Notice of References Cited	Application/Control No. 09/308,478	Applicant(s)/Patent Under Reexamination WEVER ET AL.	
	Examiner Douglas W. Sergent	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification	
	A	US-US006212668B-	04-2001	Tse et al.	716	7
	B	US-US006031979A-	02-2000	Hachiya	716	7
	C	US-US005805459A-	09-1998	Kapoor	703	14
	D	US-US005761077A-	06-1998	Shackleford	716	7
	E	US-US005748844A-	05-1998	Marks	706	45
	F	US-US005587922A-	12-1996	Hendrickson et al.	712	13
	G	US- -				
	H	US- -				
	I	US- -				
	J	US- -				
	K	US- -				
	L	US- -				
	M	US- -				

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification	
	N	- -					
	O	- -					
	P	- -					
	Q	- -					
	R	- -					
	S	- -					
	T	- -					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Kapp et al., "An Improved Cost Function for Static Partitioning of Parallel Circuit Simulations Using Conservative Sychronization Protocol", Proceedings Ninth Workshop on Parallel and Distribued Simulation, pp. 78-85, June 1995.
	V	Peinado et al., "Parallel "Go With the Winners' Algorithms in the LogP Model", Proceedings International Parallel Processing Symposium, pp. 656-664, April 1997.
	W	Buch et al., "A Parallel Graph Partitioner on a Distrubuted Memory Processor", Proceedings Fifth Symposium on Frontiers of Massively Parallel Computation, pp. 360-366, February 1995.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.